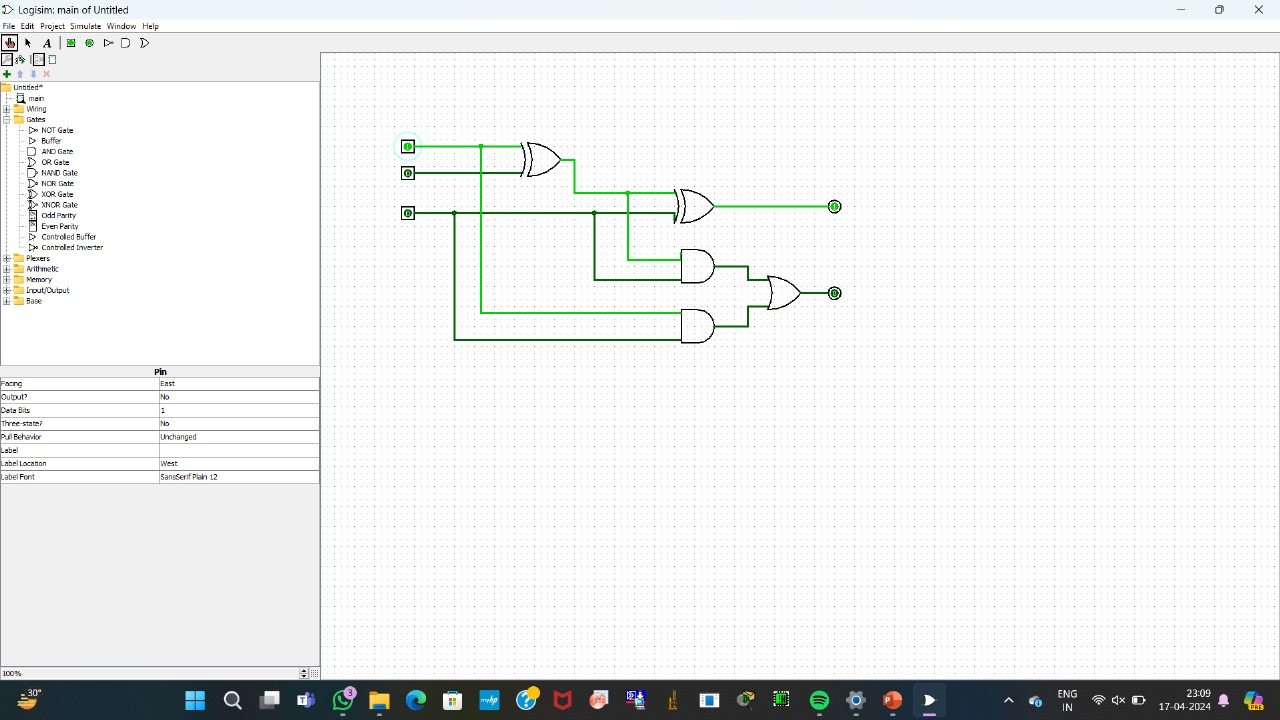
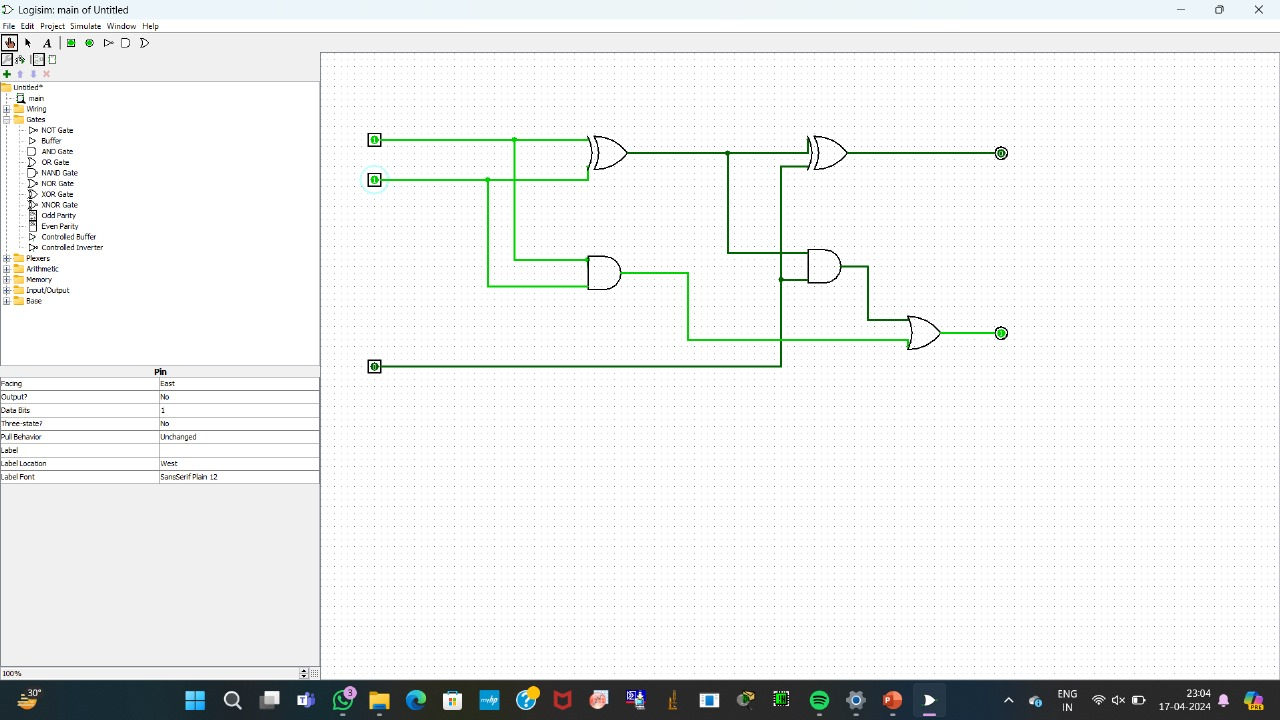
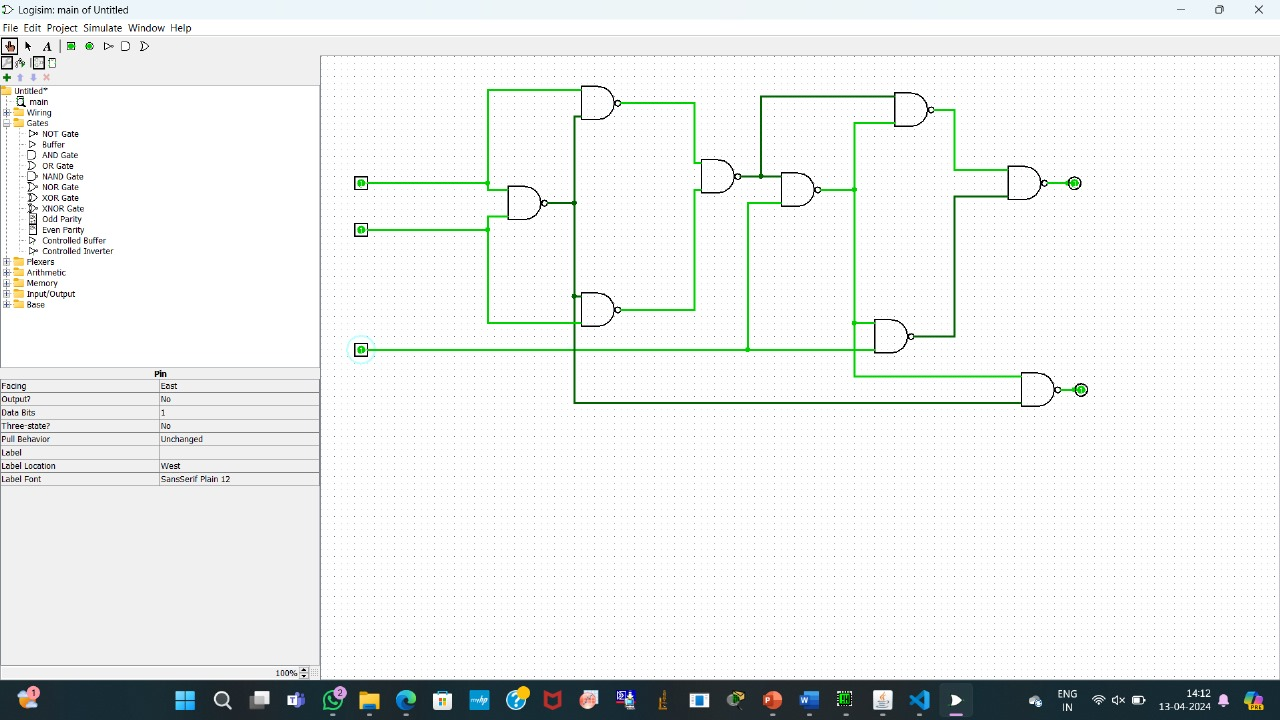
**FULL ADDER**

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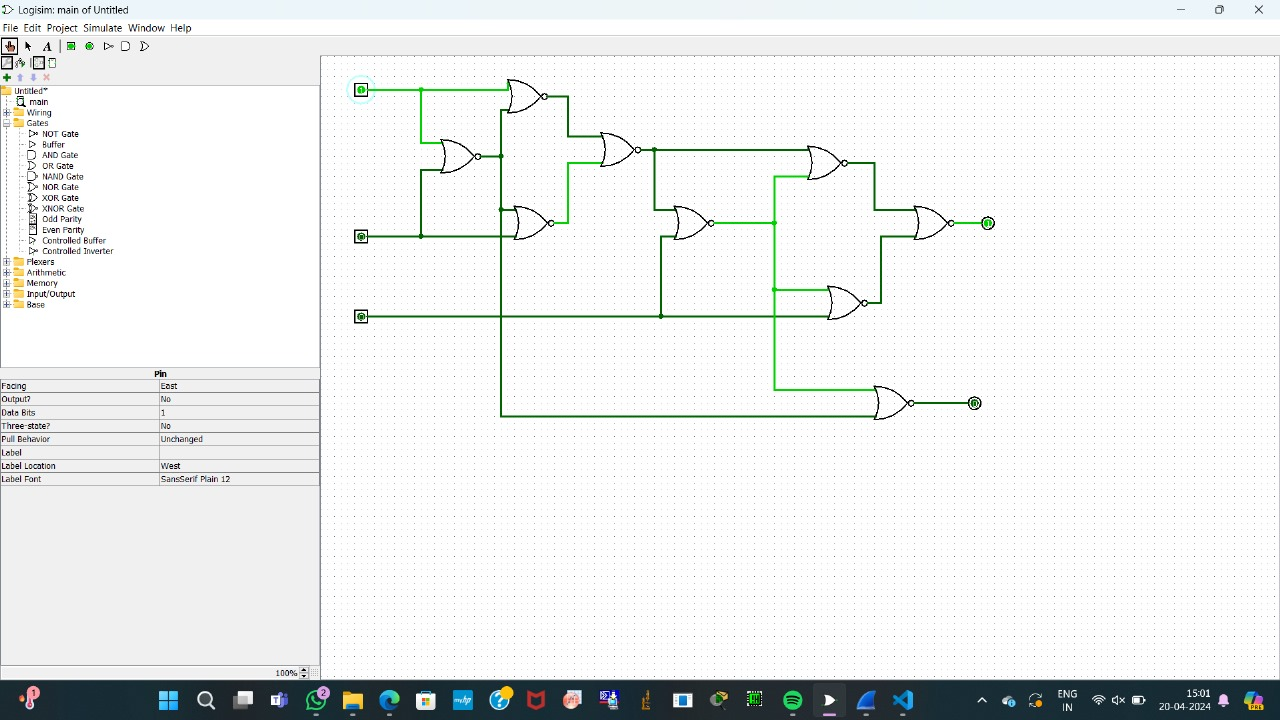
**1.Normal full adder**

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**2. Using two half adders**

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**3.Using NAND gate**

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**4.Using NOR Gate**